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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/882,046
		Filing Date	June 18, 2001
		First Named Inventor	Marcos C. Tzannes et al.
		Group Art Unit	2133
		Examiner Name	Not yet assigned
		Attorney Docket Number	081513-77
Sheet	1	of	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Bond J. W. et al. "Constructing Low-Density Parity-Check Codes" pages 260-262, XP-002182318.	
		Cao M. et al. "Multidimensional TCM schemes for ADSL" Electronics Letters, 27 May 1999, Vol.35, no. 11, pages 870-872, XP-002182320.	
		Divsalar D. et al. "Serial Concatenated Trellis Coded Modulation with Rate-1 Inner Code" pages 777-782, XP-002182317.	
		Jung-Fu Cheng "On the Construction of Efficient Multilevel Coded Modulations" Proceeding s of IEEE International Symposium On Information Theory, Ulm, Germany, 29 June - 4 July 1997, page 522, XP-002182321.	
		MacKay D.J.C. "Good Error-Correcting Codes Based on Very Sparse Matrices" IEEE Transactions On Information Theory, IEEE Inc. New York, US, vol. 45, no. 2, March 1999, pages 399-431, XP-002143042.	
		Li Ping et al. "Low density parity check codes with semi-random parity check matrix" Electronics Letters, 7 Jan. 1999, IEE, UK, vol. 35, no. 1, pages 38-39, XP-002182319.	
		Copy of the international Search Report dated November 28, 2001	

Examiner Signature		Date Considered	3/18/2001
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